



DT1140-04LP

#### **Features**

- Clamping Voltage:9V at 10A 100ns TLP; 9V at 6A 8µs/20µs
- IEC 61000-4-2 (ESD): Air +20/-18kV, Contact +20/-16kV •
- IEC 61000-4-5 (Lightning): ±6A (8/20µs)
- 4 Channels of ESD protection
- Low Channel Input Capacitance of 0.5pF Typical
- TLP Dynamic Resistance: 0.25Ω
- Typically Used for High Speed Ports such as USB 2.0, DVI, HDMI, Ethernet Port, IEEE, MDDI, PCI Express , SATA/ eSATA
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

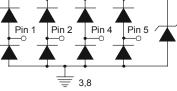
### **4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY**

### **Mechanical Data**

- Case: U-DFN2510-10
- Case Material: Molded Plastic, "Green" Molding Compound • UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020 .
- Terminals: NiPdAu over Copper leadframe (Lead Free Plating)
- Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.038 grams (approximate)

Pin #	Description	]	10	9	8	7	6	_					
1, 2, 4, 5	I/O		<u></u>	<u> </u>	L	- <u>1</u> -2	<u> </u>		Pin 1	Pin 2	Pin 4	Pin 5 🦯	$\sim$
6, 7, 9, 10	No Connection	1			<u></u>		·	_	<u> </u>	<u> </u>			
3, 8	Vss		1	2	3	4	5						
		Ľ		2	3	4	5						l

Pin Description (Top View)



**Device Schematic** 

## Ordering Information (Note 4)

h					
Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DT1140-04LP-7	Standard	BC2	7	8	3,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. 2. See http://www.diodes.com/quality/lead free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

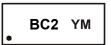
and Lead-free

Notes:

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



BC2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key

Year	20	13	20	14	20	15	20	16	20	17	20	18
Code	A	A	E	3	(	2	[	)	E		F	-
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current, per IEC 61000-4-5	IPP	6	А	I/O to V <sub>SS</sub> , 8/20µs
Peak Pulse Power, per IEC 61000-4-5	P <sub>PP</sub>	60	W	I/O to V <sub>SS</sub> , 8/20µs
Operating Voltage (DC)	V <sub>DC</sub>	6	V	I/O to V <sub>SS</sub>
ESD Protection – Contact Discharge, per IEC 61000-4-2	V <sub>ESD_Contact</sub>	+20/-16	kV	I/O to V <sub>SS</sub>
ESD Protection – Air Discharge, per IEC 61000-4-2	V <sub>ESD_Air</sub>	+20/-18	kV	I/O to V <sub>SS</sub>
Operating Temperature	T <sub>OP</sub>	-55 to +85	°C	—
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C	—

## **Thermal Characteristics**

	1		
Characteristic	Symbol	Value	Unit
Power Dissipation Typical(Note 5)	PD	350	mW
Thermal Resistance, Junction to Ambient Typical(Note 5)	$R_{ ext{ heta}JA}$	360	°C/W

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

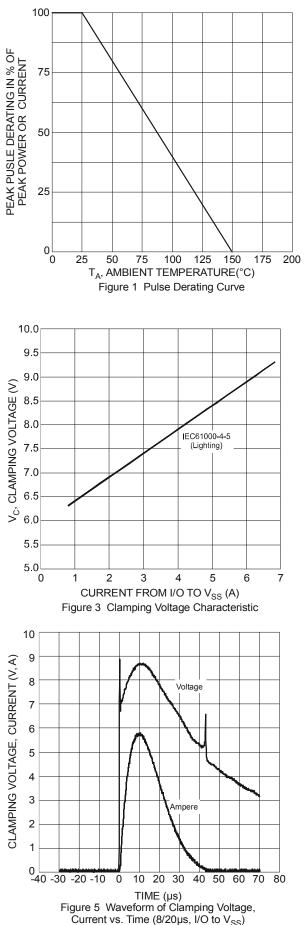
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	V <sub>RWM</sub>	—	—	5.5	V	I <sub>R</sub> =1mA, , I/O to V <sub>SS</sub>
Reverse Current (Note 6)	I <sub>R</sub>	—	_	0.5	μA	$V_R$ = 5V, I/O to $V_{SS}$
Reverse Breakdown Voltage	V <sub>BR</sub>	6	_	_	V	I <sub>R</sub> = 1mA, I/O to V <sub>SS</sub>
Forward Clamping Voltage	VF	-1.0	-0.85	_	V	$I_F$ = -15mA, I/O to V <sub>SS</sub>
Holding Voltage	V <sub>H</sub>	5.5	_	_	V	—
Reverse Clamping Voltage (Note 7)	Vc	_	6.4	_	V	I <sub>PP</sub> = 1A, I/O to V <sub>SS</sub> , 8/20µs
Reverse Clamping Voltage (Note 7)	Vc	—	9	10	V	I <sub>PP</sub> = 6A, I/O to V <sub>SS</sub> , 8/20µs
Trigger Voltage	V <sub>TRIG</sub>	_	_	9.5	V	—
ESD Clamping Voltage	V <sub>ESD</sub>	_	9	_	V	TLP, 10A, tp = 100 ns, I/O to V <sub>SS</sub>
Dynamic Reverse Resistance	R <sub>DIF-R</sub>	_	0.25	_	Ω	TLP, 10A, tp = 100 ns, I/O to $V_{SS}$
Dynamic Forward Resistance	R <sub>DIF-F</sub>	—	0.25	_	Ω	TLP, 10A, tp = 100 ns, V <sub>SS</sub> to I/O
Channel Input Capacitance	C <sub>I/O</sub>	_	0.5	0.65	pF	V <sub>I/O</sub> = 2.5V, V <sub>SS</sub> = 0V, f = 1MHz

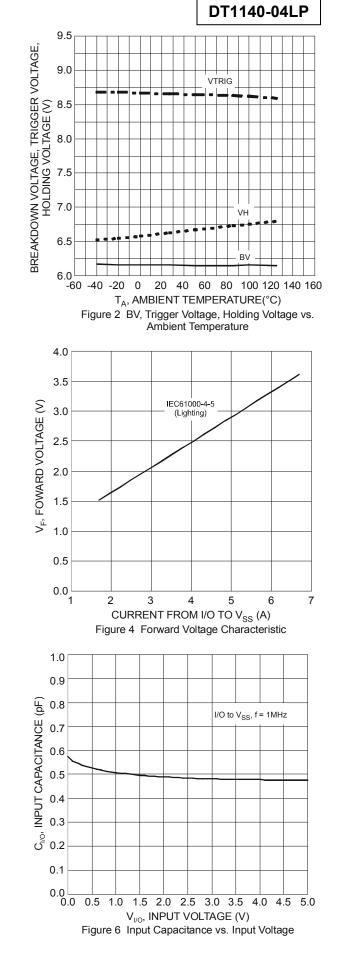
Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.

6. Short duration pulse test used to minimize self-heating effect.

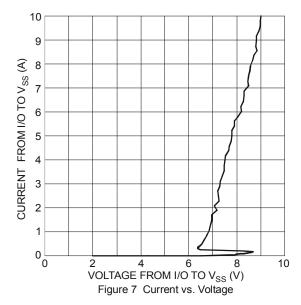
7. Clamping voltage value is based on an  $8x20\mu s$  peak pulse current  $(I_{pp})$  waveform.





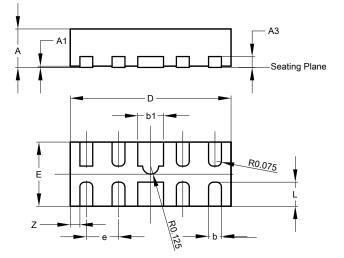






# Package Outline Dimensions

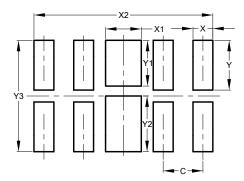
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



U-DFN2510-10								
Dim	Min	Max	Тур					
Α	0.545	0.605	0.575					
A1	0	0.05	0.03					
A3			0.13					
b	0.15	0.25	0.20					
b1	0.35	0.45	0.40					
D	2.450	2.575	2.500					
е			0.50					
Е	0.950	1.075	1.000					
L	0.325	0.425	0.375					
Z	-	-	0.150					
All D	All Dimensions in mm							

## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)				
С	0.500				
Х	0.250				
X1	0.450				
X2	2.250				
Y	0.625				
Y1	0.575				
Y2	0.700				
Y3	1.400				



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