







Description

The C0727J5003AHF is a low cost, low profile sub-miniature high performance 3 dB coupler in an easy to use surface mount package. The C0727J5003AHF is ideal for balanced power and low noise amplifiers, plus signal distribution and other applications where low insertion loss and tight amplitude and phase balance are required. The C0727J5003AHF is available on tape and reel for pick and place high volume manufacturing.

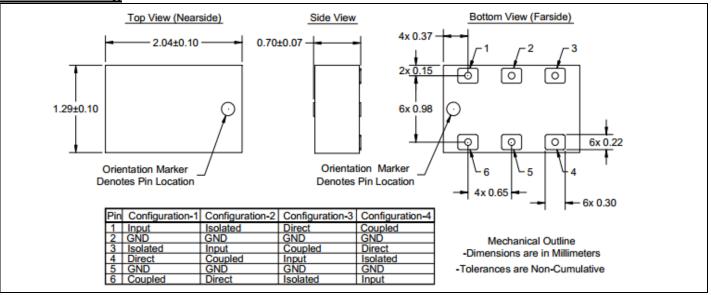
All of the Xinger components are constructed from ceramic filled PTFE composites which possess excellent electrical and mechanical stability. All parts have been subjected to rigorous qualification testing and units are 100% RF tested.

Detailed Electrical Specifications: Specifications subject to change without notice.

		ROOM (25°C)			
Features:	Parameter	Min.	Тур.	Max	Unit
• 700 – 2700 MHz	Frequency	700		2700	MHz
0.7mm Height Profile Use Indian 8 Law Lag	Port Impedance		50		Ω
High Isolation & Low LossLTE Bands: 24	Return Loss	23	31		dB
Surface Mountable	Isolation	23	28.8		dB
Tape & Reel	Insertion Loss*		0.7	0.8	dB
Non-conductive Surface	Amplitude Balance		2.3	2.8	dB
RoHS Compliant	Phase Balance (relative to 90°)		6.5	11	Degrees
Halogen-Free 400% PF Taggetard	` ·		0.5		· ·
• 100% RF Tested	Power Handling @85C			2	Watts
• -55°C to 105°C	Operating Temperature	-55		+105	°C

^{*} Insertion Loss stated at room temperature (Insertion Loss is approximately 0.1 dB higher at +85 °C)

Outline Drawing



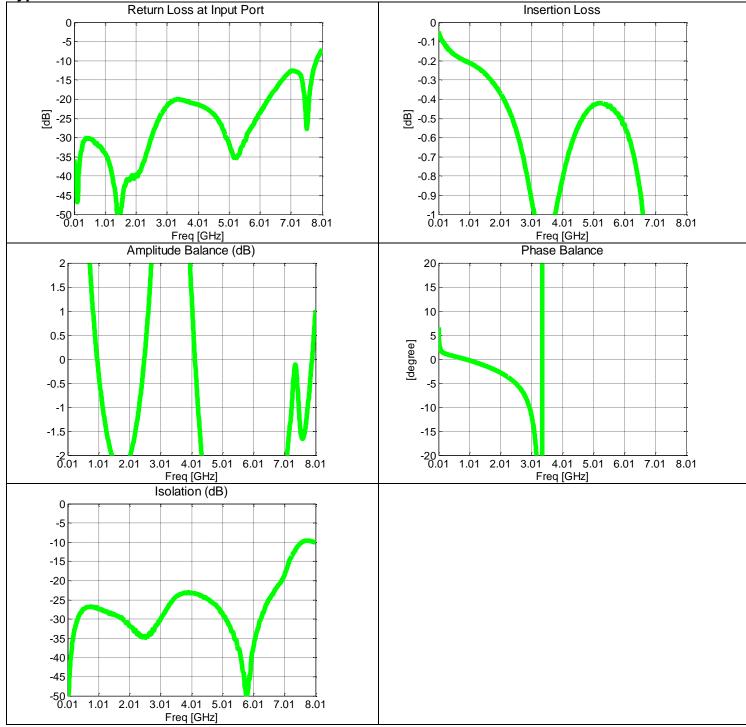


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Model C0727J5002AHF







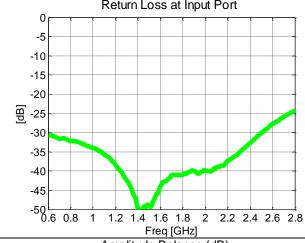


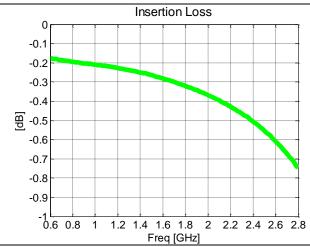
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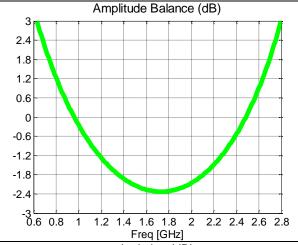


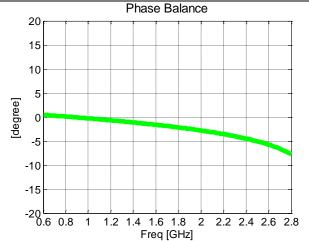


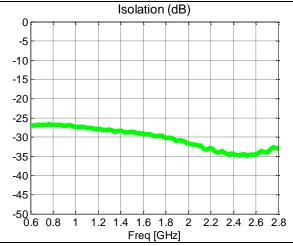












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Definition of Measured Specifications

Parameter	Definition	Mathematical Representation i, j, k, m is denoted as the port index of input, isolated, direct and coupled port for specific pin configuration shown in the table
Return Loss	The impedance match of the coupler to a 50Ω system. Return Loss is an alternate means to express VSWR.	$20\log_{10}(\left S_{ii}\right)$
Isolation	The input power divided by the sum of the power at the two output ports.	$20\log_{10}\left S_{ji}\right $
Insertion Loss	The input power divided by the sum of the power at the two output ports.	$10\log_{10}(\left S_{mi}\right ^{2}+\left S_{ki}\right ^{2})$
Amplitude Balance	The difference in power between the two outputs.	$10\log_{10}\left(\frac{S_{ki}}{S_{mi}}\right)$
Phase Balance	The difference in phase angle between the two output ports.	$\angle S_{ki} - \angle S_{mi} + 90^{\circ}$

^{*100%} RF test is performed per spec definition for pin configuration 1 and port 1 (input port) is connected to pin 1, port 2 (isolated port) is connected to pin 3, port 3 (direct port) is connected to pin 4 and port 4 (isolated) is connected to pin 6.



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